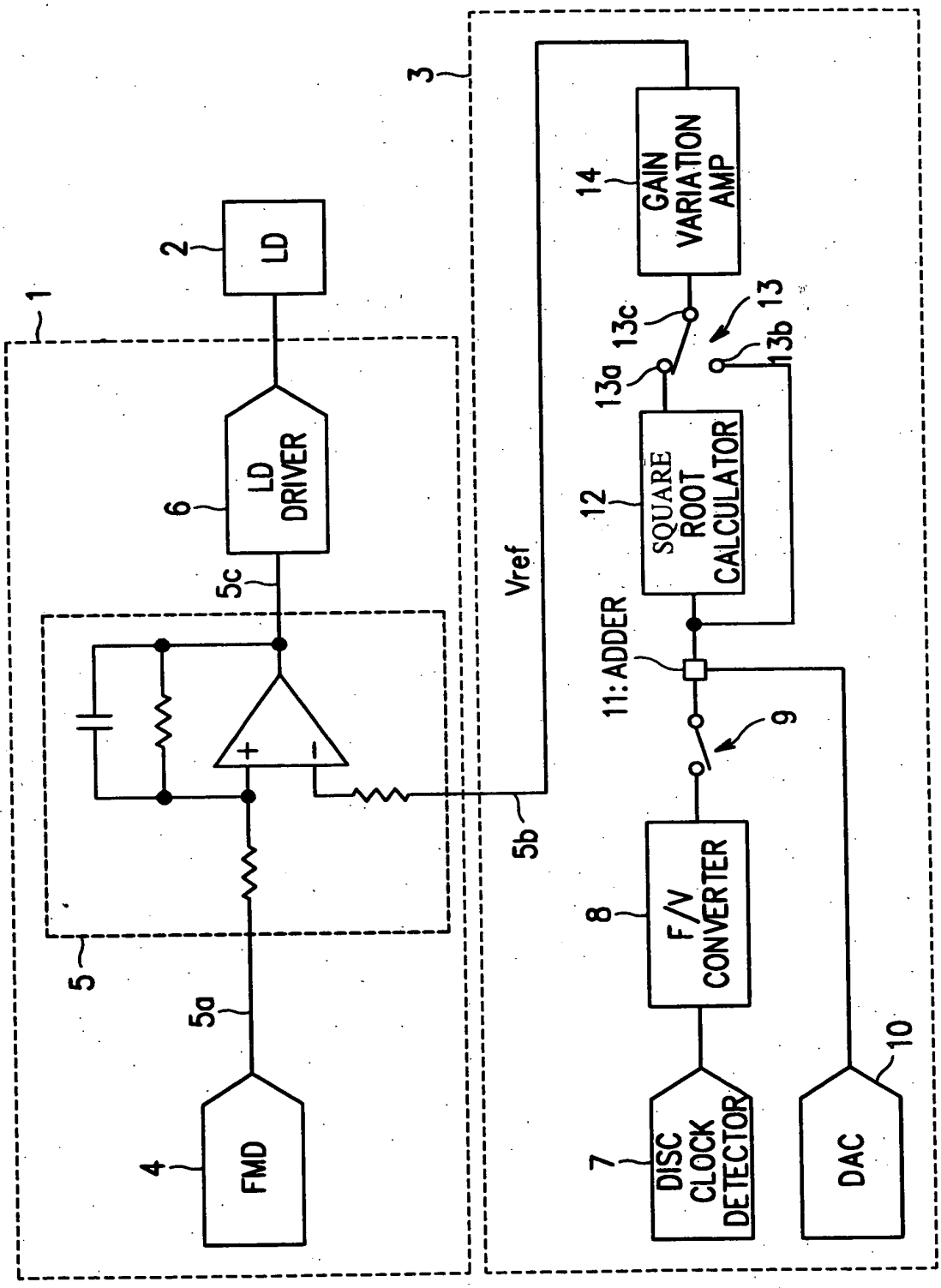
FIG. 1


1
2
LD
6
LD DRIVER
5c
5
5a
4
FMD
3
Vref
5b
14
GAIN VARIATION AMP
13
13c
13a
13b
12
SQUARE ROOT CALCULATOR
11: ADDER
9
8
F/V CONVERTER
7
DISC CLOCK DETECTOR
10
DAC